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Term	Documents
(4 AND 25).PGPB,USPT.	18
(L4 AND L25).PGPB,USPT.	18

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7:62/34

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L29

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Set Name	Query	Hit Count	Set Name result set
side by side			
DB=PGPB,USPT; PLUR=YES; OP=OR			
L29	14 and 125	18	L29
L28	14 and 124	24	L28
L27	118 and L25	93	L27
L26	118 and L24	133	L26
L25	(712/20-225)[CCLS]	8916	L25
L24	(712/2-300)[CCLS]	13376	L24
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L23	("all" or every or each) near12 (nop\$1 or "no" near1 operation\$1) near25 (1 or 0 or one or zero\$3 or set or reset\$4 or mask\$5)	53	L23
L22	("all" or every or each) near12 (nop\$1 or "no" near1 operation\$1) near25 mask\$5	0	L22
L21	119 not 20	15	L21

<u>L20</u>	L19 and (vliw or very near1 (large or long) near1 instruction\$1)	13	<u>L20</u>
<u>L19</u>	(all or plur\$7 or multipl\$6 or two or dual or second or every or each) near10 (nop\$1) near12 (detect\$3 or determin\$7)	57	<u>L19</u>
<u>L18</u>	(nop\$1) near12 (detect\$3 or determin\$7)	513	<u>L18</u>
<u>L17</u>	L16 and (vliw or very near1 (large or long) near1 instruction\$1)	8	<u>L17</u>
<u>L16</u>	(nop\$1) near12 (each or every or all)	66	<u>L16</u>
<u>L15</u>	L12 and (vliw or very near1 (large or long) near1 instruction\$1)	68	<u>L15</u>
<u>L14</u>	L12 near75 (vliw or very near1 (large or long) near1 instruction\$1)	7	<u>L14</u>
<u>L13</u>	L12 near25 (vliw or very near1 (large or long) near1 instruction\$1)	7	<u>L13</u>
<u>L12</u>	(combin\$7 or compar\$6 or mask\$5) near15 nop\$1	458	<u>L12</u>
<u>L11</u>	L10 near18 (single or final or overal\$5 or sum\$6 or "AND" or "ANDed" or combin\$6)	3	<u>L11</u>
<u>L10</u>	mask\$5 near10 nop\$1	84	<u>L10</u>
<u>L9</u>	L8 not 16	18	<u>L9</u>
<u>L8</u>	("all" or every) near12 (nop\$1 or "no" near1 operation\$1) and (vliw or very near1 (large or long) near1 instruction\$1)	21	<u>L8</u>
<u>L7</u>	L4 and (vliw or very near1 (large or long) near1 instruction\$1)	5	<u>L7</u>
<u>L6</u>	L5 and (vliw or very near1 (large or long) near1 instruction\$1)	5	<u>L6</u>
<u>L5</u>	L4 and (nop\$1 or mask\$6)	60	<u>L5</u>
<u>L4</u>	("all" or every or "AND" or "ANDed") near12 (nop\$1 or "no" near1 operation\$1) near15 ("true" or one or "1" or mask\$5 or set\$4 or reset\$5)	120	<u>L4</u>
<u>L3</u>	("all" or every) near12 (nop\$1 or "no" near1 operation\$1) near15 mask\$5	0	<u>L3</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L2</u>	5893143.pn.	1	<u>L2</u>
<u>L1</u>	893143.pn.	0	<u>L1</u>

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» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard




- ☐ **Hierarchical instruction encoding for VLIW digital signal processors**  
 Chia-Hsien Liu; Tay-Jyi Lin; Chie-Min Chao; Pi-Chen Hsiao; Li-Chun Lin; Shin-Kai Chen; Chao-Wei Jen;  
Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on  
 23-26 May 2005 Page(s):3503 - 3506 Vol. 4  
 Digital Object Identifier 10.1109/ISCAS.2005.1465384  
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- ☐ **A method of speculative dual-path execution for VLIW processors**  
 Shimajiri, H.; Yoshida, T.;  
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 Volume B, 21-24 Nov. 2004 Page(s):195 - 198 Vol. 2  
 Digital Object Identifier 10.1109/TENCON.2004.1414565  
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- ☐ **An efficient VLIW DSP architecture for baseband processing**  
 Tay-Jyi Lin; Chin-Chi Chang; Chen-Chia Lee; Chein-Wei Jen;  
Computer Design, 2003. Proceedings. 21st International Conference on  
 13-15 Oct. 2003 Page(s):307 - 312  
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- ☐ **Higher performance and lower power enhancements to VLIW architectures**  
 Gass, W.;  
Signal Processing Systems, 2001 IEEE Workshop on  
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 Digital Object Identifier 10.1109/SIPS.2001.957342  
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- ☐ **A 4-way VLIW embedded multimedia processor**  
 Suga, A.; Sukemura, T.; Takahashi, H.; Wada, K.; Miyake, H.; Nakamura, Y.; Takebe, Y.; Azegami M.; Okano, T.; Shiota, T.; Saito, M.; Wakayama, S.; Ozawa, T.; Satoh, T.; Sakurai, A.; Katayama, T. K.;  
Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 2000. IEEE International

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